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29. (New) A drive circuit for a high input resistance transistor device as claimed in claim 28,
wherein said slave negative voltage supply device further includes a capacitor connected in
parallel between ends of the inductor.

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30. (New) A drive circuit for a high input resistance transistor device as claimed in claim 24,
wherein said slave negative voltage supply device includes an inductor connected in series
between the photoelectric conversion device and the gate of the high input resistance transistor,
said inductor having a polarity opposite to a drive current direction.

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31. (New) A drive circuit for a high input resistance transistor device as claimed in claim 30,
wherein said slave negative voltage supply device further includes a resistor connected in parallel
between the photoelectric conversion device and the high input resistance transistor device.

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32. (New) A drive circuit for a high input resistance transistor device as claimed in claim 30,
wherein said slave negative voltage supply device includes a capacitor connected in parallel
between ends of the inductor.

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33. (New) A drive circuit for a high input resistance transistor device as claimed in claim 33,
wherein said slave negative voltage supply device includes a resistor connected in parallel
between the photoelectric conversion device and the high input resistance transistor device.

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34. (New) A drive circuit for a high input resistance transistor device as claimed in claim 24,
wherein said slave negative voltage supply device includes a secondary battery system connected
in series between the photoelectric conversion device and a source of the high input transistor
device, and a resistance connected in parallel with either side of the secondary battery system.

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35. (New) A drive circuit for a high input resistance transistor device as claimed in claim 34,
wherein said secondary battery system comprises a capacitor or rechargeable secondary battery
connected in parallel with a second resistance.